

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph [0042] beginning on page 29 as follows:

[0042] The second circuit board 50 includes foils 21A and 21B of the first conductor layer~~layers 21A and 21B~~, foils 22A and 22B of the second conductor layer~~layers 22A and 22B~~, the foils 23A and 23B of the third conductor layer~~layers 23A and 23B~~, and foils 24A and 24B of the fourth conductor layer~~layers 24A and 24B~~ (See Figs. 8 to 11) in this order from the surface of the case 10 (See Figs. 1 and 2). In Figs. 8 to 11, the cross section of the second circuit board 50 is shown with the surface of the case 10 facing upward.

Please amend the paragraph [0044] beginning on page 30 as follows:

[0044] In the areas where the first foils 21A, 22A, 23A and 24A of the first to fourth conductor layers are overlapped, the first ballast capacitor CB1 is composed of the capacitance among the foils (See shaded parts CB1 shown in Fig. 7, and see Figs. 8 and 9). The first ballast capacitor CB1 is substantially equivalent to a parallel connection of main three inter-foil capacitances, that is, a capacitance between the first and second conductor layers (21A and 22A), a capacitance between the second and third conductor layers (22A and 23A), and a capacitance between the third and fourth conductor layers (23A and 24A).

In a manner similar to above, in the areas where the ~~first-second~~ foils 21B and 23B and the ~~second-first~~ foils 22A and 24A are overlapped, the second ballast capacitor CB2 is composed (See a shaded part CB2 shown in Fig. 7, and see Figs. 9 and 10), and in the areas where the second foils 21B, 22B, 23B and 24B are overlapped, the third ballast capacitor CB3 is composed (See a shaded part CB3 shown in Fig. 7, and see Fig. 11). In this way, each of the three ballast capacitors CB1, CB2 and CB3 is configured as a so-called comb capacitor.

Please amend the paragraph [0048] beginning on page 33 as follows:

[0048] The second circuit board 50 includes foils 21A and 21B of the first conductor layer~~layers~~ 21A and 21B, the foils 22A and 22B of the second conductor layer~~layers 22A and 22B~~, the foils 24A and 24B of the third conductor layer~~layer 23A and 23B~~, and foils 24A and 24B of the fourth conductor layer~~layer 24A and 24B~~ (See Figs. 13 to 16) in this order from the surface of the case 10 (See Figs. 1 and 2). In Figs. 13 to 16, the cross section of the second circuit board 50 is shown with the surface of the case 10 facing upward.

The first and fourth conductor layers have similar patterns. In addition, the first and fourth conductor layers include the first foils 21A and 24A having similar shapes, respectively, at the same positions as viewed in the direction normal to the surfaces of the foils. Further, the first and fourth conductor layers include the second foils 21B and 24B having the same shape, respectively, at the same positions as viewed in the direction normal to the surfaces of the foils (See Fig. 12). Unlike the first foil 21A of the first conductor layer, as for the first foil 24A of the fourth conductor layer, the first foils 24A of adjoining second blocks 2 are separated from each other. The first foils 21A and 24A are connected to each other via the fourth through hole E4 (See Figs. 12 and 13), and the second foils 21B and 24B are connected via the fifth through hole E5 (See Figs. 12 and 16).

The second and third conductor layers have the same pattern. In addition, the second and third conductor layers include the first foils 22A and 23A having the same shape, respectively, at the same positions as viewed in the direction normal to the surfaces of the foils. Further, the second and third conductor layers include the second foils 22B and 23B having the same shape, respectively, at the same positions as viewed in the direction normal to the surfaces of the foils (See Fig. 12). The first foils 22A and 23A are connected via the third through hole E3 (See Figs. 12 and 14), and the second foils 22B and 23B are connected via the second through hole E2 (See Figs. 12 and 15).